

PTO-1449		Application No. 09/617,624		Applicant(s) Carroll, et al.			
<b>Information Disclosure Citation in an Application</b>		Docket Number 068167.0104		Group Art Unit 2751	Filing Date July 17, 2000		
<b>U.S. PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE</b>
A	B	5,815,686	9/29/98	Earl, et al.	395	500	9/12/96
		4,779,188	10/18/88	Gum et al.	364	200	10/19/87
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<b>FOREIGN PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>COUNTRY</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>TRANSLATION</b>
							<b>YES</b> <b>NO</b>
C		EP 0 645 701 A2	26/09/94	EPO	G06F	9/455	X
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<b>NON-PATENT DOCUMENTS</b>							
<b>Technology Center 2600</b>							
		<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>					
D		PCT International Search Report in International Application No. PCT/US 01/22276, International filing date 16/07/01, mail date 07/03/02.					
E		Traut E, "Building the Virtual PC," <i>Byte</i> , McGraw-Hill Inc., Vol. 22, No. 11, pp. 51-52, 1 November 1997.					
F		"Intel386 DX Microprocessor," <i>Intel</i> , pp. 32-58, 31 December 1995.					
G		"Macintosh and Technology: Changing Chips in the Middle of the Stream, or Apple Takes a Risc," URL: <a href="http://www.btech.co/changingchips.html">www.btech.co/changingchips.html</a> , paragraphs '0006!'-'0007!', retrieved 12/10/01.					
H		"M68040 User's Manual," Motorola, Inc., Chapter 3, copyright 1990, revised 1992, 1993.					
I		Osisek DL et al., "ESA/390 Interpretive-Execution Architecture, Foundation for VM/ESA," <i>IBM Systems Journal</i> , Vol. 30, No. 1, pp. 34-51, 1991.					
J		Shang Rong Tsai et al., "On the Architectural Support for Logical Machine Systems," <i>Microprocessing and Microprogramming</i> , Vol. 22, No. 2, pp. 81-96, February 1988.					
EXAMINER <i>Rm</i>				DATE CONSIDERED <b>3-21-04</b>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							

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## U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
PDR	A	5,063,499	11/05/1991	Garber	395	500	01/09/1989
	B	5,699,539	12/16/1997	Garber et al.	395	402	05/07/1996
	C	5,617,552	04/01/1997	Garber et al.	395	401	02/29/1996
	D	5,675,382	10/07/1997	Bauchspies	348	390	04/08/1996
	E	5,768,593	06/16/1998	Walters et al.	395	705	03/22/1996
	F	6,008,847	12/28/1997	Bachspies	348	391	03/03/1997
	G	6,115,054	09/05/2000	Giles	345	522	12/29/1998


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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

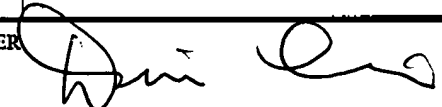
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Full  File	A	5,063,499	11/05/1991	Garber	395	500	01/09/1989	
	B	5,699,539	12/16/1997	Garber et al.	395	402	05/07/1996	
	C	5,617,552	04/01/1997	Garber et al.	395	401	02/29/1996	
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	F	6,008,847	12/28/1997	Bachspies	348	391	03/03/1997	
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PTO-1419 INFORMATION DISCLOSURE CITATION in an Application JAN 17 2001 TRADEMARK OFFICE	Application No. 09/617,624	Applicant(s) Tim Carroll, Aaron Giles	
	Docket Number 068167.0104	Group Art Unit 2751	Filing Date July 17, 2000

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C	5367628	11/22/94	Ote et al.	345	501	10/15/92
D	5448264	9/5/95	Pinedo et al.	345	508	6/14/93
E	5452456	9/19/95	Mourey et al.	713	100	12/18/92
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J	5752275	5/12/98	Hammond	711	207	7/14/97
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R	6067618	5/23/00	Weber	713	1	3/26/98

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T "M68060 User's Manual," Motorola, 1994, pp. i-xviii; Section 4, Memory Management Unit, pp. 4-1 to 4-30, <a href="http://www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf">http://www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/68K-COLDFIRE/M680X0/MC68060UM.pdf</a> .	
U "MPC750, RISC Microprocessor User's Manual," Motorola, 8/97, Contents, pp. iii-xvi; Chapter 5, Memory Management, pp. 5-1 to 5-34; Glossary, pp. Glossary-1 to Glossary-13, <a href="http://www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf">http://www.motorola.com/brdata/PDFDB/MICROPROCESSORS/32_BIT/POWERPC/MPC7XX/MPC750UM.pdf</a> .	

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